

IN THE CLAIMS

Please amend the claims as follows.

For the Examiner's convenience, a list of all claims is included below.

1. (Original) A method comprising:

monitoring processor utilization of a computer system having a processor, the processor having a plurality of performance levels; and

automatically transitioning the processor to a higher performance level if it is determined that the processor utilization has remained above a switch-up level for a specified time.
2. (Original) The method of claim 1, wherein the number of performance levels is two.
3. (Original) The method of claim 1, wherein the switch-up level is approximately 95% of a current processor performance level.
4. (Original) The method of claim 1, further comprising:

automatically transitioning the processor to a next lower performance level, if any, if it is determined that the processor utilization has remained below a switch-down level for a specified time.
5. (Original) The method of claim 4, wherein the switch-down level is approximately 95% of the next lower processor performance level.
- 6 – 8 (Canceled)

9. (Original) A machine-readable medium that provides executable instructions, which when executed by a processing system, cause said processing system to perform a method, the method comprising:

periodically monitoring processor utilization of a computer system having a processor, the processor having a plurality of performance levels; and

automatically transitioning the processor to a higher performance level if it is determined that the processor utilization has remained above a switch-up level for a specified time.

10. (Original) The machine-readable medium of claim 9, wherein the number of performance levels is two.

11. (Original) The machine-readable medium of claim 9, wherein the switch-up level is approximately 95% of a current processor performance level.

12. (Original) The machine-readable medium of claim 9, further comprising:

automatically transitioning the processor to a next lower performance level, if any, when the processor utilization has remained below a switch-down level for a specified period of time.

13. (Original) The machine-readable medium of claim 12 wherein the specified period of time that the processor utilization has remained above a switch-up level to transition the processor to a higher performance level is different than the specified period of time that the processor utilization has remained below a switch-down level to transition the processor to a next lower performance level.

14. (Original) The machine-readable medium of claim 12, wherein the switch-down level is approximately 95% of the next lower processor performance level.

15. (Original) The machine-readable medium of claim 12, wherein the specified period of time that the processor utilization has remained above a switch-up level to transition the processor to a higher performance level is greater than a processor-utilization monitoring periods.

16. (Original) The machine-readable medium of claim 15, wherein the specified period of time that the processor utilization has remained below a switch-down level to transition the processor to a next lower performance level equal to the processor-utilization monitoring period.